L Numbe			DB	Time stamp
-	-	5108939.pn. and flash	USPAT;	2003/06/05 09:5
		BEST AVAILABLE COPY	US-PGPUB;	
		TO ALLABLE CODY	EPO; JPO;	
		COPY	DERWENT;	
2	1 0		IBM_TDB	
	`	6160287.pn. and nonvolatile	USPAT;	2003/06/05 09:53
			US-PGPUB;	
			EPO; JPO;	
	1		DERWENT;	1
_	224	(nonvolatile adi	IBM_TDB	
		(nonvolatile adj memory adj cell) and (floating adj gate) and spacer and	USPAT;	2003/06/03 11:25
		(control adj gate) and spacer and	US-PGPUB;	
		(concross day gate)	EPO; JPO;	
			DERWENT;	
_	2	(nonvolatile adj memory adj cell) and	IBM_TDB	
		(main adj floating adj gate)	USPAT;	2003/06/04 10:54
		( adj floating adj gate)	US-PGPUB;	
			EPO; JPO;	
	ļ		DERWENT;	i
_	3	(nonvolatile adj memory adj cell) and	IBM_TDB	1
		(connecting adj floating adj gate)	USPAT;	2003/06/03 11:28
	J	, and troacting adj gate)	US-PGPUB;	
	1		EPO; JPO;	
	1		DERWENT;	
-	296	(nonvolatile adj memory adj cell) and	IBM_TDB	
	1	(floating adj gate) and (self adj aligned)	USPAT;	2003/06/04 10:13
	İ	j and gate, and (sell adj aligned)	US-PGPUB;	
			EPO; JPO;	}
			DERWENT;	
	652	257/314.ccls.	IBM_TDB	
			USPAT;	2003/06/04 10:15
	1		US-PGPUB;	
			EPO; JPO;	
	1		DERWENT;	
	1580	257/316.ccls.	IBM TDB	000040545
			USPAT;	2003/06/04 10:21
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	355	257/316.ccls. and spacers	IBM_TDB USPAT;	2002/06/2:
		•	US-PGPUB;	2003/06/04 10:40
			EPO; JPO;	
			DERWENT;	]
			IBM TDB	] ]
	99	257/317.ccls. and spacers	USPAT;	2003/05/04 20 15
		-	US-PGPUB;	2003/06/04 10:42
	[		EPO; JPO;	
ł			DERWENT;	
		0.55	IBM TDB	
ĺ	43	257/318.ccls. and spacers	USPAT;	2003/06/04 30 41
		-	US-PGPUB;	2003/06/04 10:44
	ļ		EPO; JPO;	
	1		DERWENT;	
1		0.50 (0.0	IBM TDB	
1	81	257/319.ccls. and spacers	USPAT;	2003/06/04 10:47
	1	•	US-PGPUB;	2003/00/04 10:47
			EPO; JPO;	
			DERWENT;	
	60	057/000	IBM TDB	
	68	257/320.ccls. and spacers	USPAT;	2003/06/04 10:49
1		İ	US-PGPUB;	2003/00/04 10:49
1	- 1		EPO; JPO;	
1	į		DERWENT;	
1			IBM TDB	
	170   2	257/321.ccls. and spacers	USPAT;	2002/06/04 10 ==
		-	US-PGPUB;	2003/06/04 10:52
	1	·	EPO; JPO;	
1	1		DERWENT;	
			IBM TDB	]
			TOTAL TOD	•,

Search History 6/5/03 10:15:32 AM Page 1

-	53	1257/222		
	33	257/322.ccBEST AVAILABLE COPY	USPAT;	2003/06/04 10:53
		TO MANIEMBLE COPY	US-PGPUB;	
			1210, 010,	
			DERWENT;	
-	1	(nonvolatile adi	IBM_TDB	
	1	(nonvolatile adj memory adj cell) and (contacting adj floating adj gate)	USPAT;	2003/06/04 10:55
	1	(contacting adj floating adj gate)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
-	16	(nonvolatile adj memory adj cell) and	IBM_TDB	
		(contact adj floating adj gate)	USPAT;	2003/06/04 10:55
1		( tombast day ribating adj gate)	US-PGPUB;	
			EPO; JPO;	
1			DERWENT;	
-	6	(nonvolatile adj memory adj cell) and (via	IBM_TDB	0000 /00 /00
		adj floating adj gate)	USPAT;	2003/06/04 11:13
		g cary and gate,	US-PGPUB;	•
			EPO; JPO; DERWENT;	
	1		IBM TDB	
-	0	(nonvolatile adj memory adj cell) and	USPAT;	2002/06/04 22 24
ļ		(polysilicon adj sidewall adj spacer)	US-PGPUB;	2003/06/04 11:14
		j and and spacely	EPO; JPO;	
1			DERWENT;	1
	1		IBM TDB	
-	180	polysilicon adj sidewall adj spacer	USPAT;	2003/06/04 11:24
ĺ		• • • • • • • • • • • • • • • • • • • •	US-PGPUB;	2003/00/04 11:24
ł	}		EPO; JPO;	
			DERWENT;	
			IBM TDB	
1	18	control adj gate adj sidewall adj spacer	USPAT;	2003/06/04 11:22
	l		US-PGPUB;	
	ļ		EPO; JPO;	
			DERWENT;	
<b> </b> _	859	polygilian adi ana	IBM_TDB	
	339	polysilicon adj spacer	USPAT;	2003/06/04 11:25
			US-PGPUB;	
			EPO; JPO;	
	]		DERWENT;	
[ <b>–</b>	221	(polysilicon adj spacer) and (control adj	IBM_TDB	
		gate) gater, and (control adj	USPAT;	2003/06/05 09:52
	1	3.00/	US-PGPUB;	
			EPO; JPO;	
	ļ		DERWENT;	
			IBM TDB	